

SHUSAKU YAMAMOTO

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Title of the Invention: DC-DRIVEN PLANAR DISPLAY DEVICE

Application Number: 3-244453

Filing Date: August 29, 1991

Inventors: J. SOTOYAMA et al.

Applicant: NIPPON MEKTRON KABUSHIKI KAISHA

Specification

[Title of the Invention] DC-DRIVEN PLANAR DISPLAY DEVICE

[Claim]

[Claim 1] A DC-driven planar display device, comprising a liquid or solid for display, optical characteristics of which are changed by a DC current, provided in a sealed state in a space between electrodes opposed to each other, the DC-driven planar display device being characterized in that an insulating layer is provided on a surface of at least one of the electrodes opposed to each other, and a maximum voltage value applied to the insulating layer is smaller than the product of a dielectric breakdown strength and a thickness of the insulating layer.

[Detailed Description of the Invention]

[0001]

[Field of the Invention] The present invention relates to a DC-driven planar display device such as an electrophoresis display device, in which a surface of an electrode in contact with a display liquid is coated with an insulating material so as to block a DC current, and thus the quality

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change of the display liquid caused by transfer of charges between the display liquid and the electrode is prevented.

[0002]

[Prior Art and its Problems] As shown in Figure 4, a DC-driven planar display device such as an electrophoresis display device includes two transparent substrates 1 and 3 which are formed of glass or the like and respectively have electrodes 2 and 4 formed on surfaces thereof opposed to each other. The transparent substrates 1 and 3 are opposed to each other with a prescribed distance kept therebetween. A display liquid 5 including a dispersion medium colored with a dye and pigment particles is sealed in a space between the substrates 1 and 3. A DC voltage is applied to the electrodes 2 and 4 opposed to each other so as to generate a DC electric field in the display liquid 5 and move the pigment particles thus charged. Thus, a desired display is realized while changing the distribution of the pigment particles.

[0003] However, the application of the DC voltage generates an electric field in the display liquid 5 and also generates a DC current in a circuit connecting the display liquid 5, the electrode 2 and 4, and a power supply. The specific resistance of the display liquid 5 used in an electrophoresis display device is usually $10^8 \Omega \cdot \text{cm}$ or more, which is relatively high, and the DC current thereof is several microamperes per square centimeters, which is relatively small. The influence of the DC current on the reliability regarding the long life of the device is quite significant.

[0004] In other words, an inflow of electrons from outside the display liquid 5 or outflow induces the oxidation and reduction of the display liquid, which causes instability,

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specific to DC driving display devices. The instability is caused due to factors such as, for example: a display change caused by a quality change in the dye; or a quality change of the surfactant used for dispersing the electrophoretic particles; extinction of the electrode caused by dissolution of the electrode components into the display liquid; or a combination of these obstacles.

[0005] In a conventional electrophoresis display device, various attempts have been made to improve the reliability during the term during which the device can be practically usable. Such attempts include, for example, increasing the specific resistance of the display liquid to reduce the DC current so that the life of the device is extended; and adopting a voltage application system which avoids superimposing of DC voltages as described in Japanese Patent Application No. 2-8875 filed by the Applicant of the present application. However, such means cannot be considered to be provide a fundamental solution and are not sufficient. According to such means, for example, the life is different based on the lot of the material; or the usable dyes, surfactants and the usable amounts are restricted, and thus the display quality itself is deteriorated.

[0006] In the case of, for example, AC-driven display devices such as liquid crystal display devices and AC-driven plasma display devices, a dielectric film is provided on a surface of the electrode in order to prevent deterioration of the display and inflow of charges into the electrode as well as to achieve other aims. In the case of a DC-driven display device, such a system is not adopted because a continuous application of a DC voltage for maintaining the display accumulates the charges in the dielectric film, which prevents effective application of the electric field on the

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display medium or causes dielectric breakdown of the dielectric film.

[0007]

[Objective and Structure of the Invention] The present invention is to provide a DC-driven planar display device for preventing quality change of a display liquid caused by transfer of the charges between the display liquid and the electrode, by blocking of a DC current by coating a surface of the electrode in contact with the display liquid with an insulating material.

[0008] In order to achieve the above-described objective, a DC-driven planar display device according to the present invention includes a liquid or solid for display, the optical characteristics of which are changed by a DC current, provided in a sealed state in a space between electrodes opposed to each other. The DC-driven planar display device includes an insulating layer provided on a surface of at least one of the electrodes which are opposed to each other, and is structured so that the maximum voltage value applied to the insulating layer is smaller than the product of a dielectric breakdown strength and a thickness of the insulating layer.

[0009]

[Examples] Figure 1 is a conceptual enlarged cross-sectional view of an important part of a DC-driven planar display device structured in accordance with the present invention. In the Figure, identical reference numerals as those in Figure 4 represent identical elements. In Figure 1, electrodes 2 and 4, required for display, are formed of a conductive material such as, for example, indium tin oxide and provided on surfaces of transparent substrates 1 and 3 which are op-

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posed to each other. The electrodes 2 and 4 are connected to an external power supply outside the transparent substrates 1 and 3 through leads not shown.

[0010] The electrodes 2 and 4 and parts of the leads are coated with insulating layers 6 and 7 which are formed separately in accordance with the present invention. Usable as the insulating materials 6 and 7 are, for example, insulating polymers such as polyethylene, polyimide, ethylene tetrafluoride and the like; and insulating metal oxides such as silicon oxide, barium titanate, titanium oxide and the like. The insulating layers 6 and 7 can be appropriately formed by a usual film formation method such as coating, printing, LB film formation, deposition, sputtering or the like. An electrophoresis display device as a DC-driven planar display device is completed by putting the display liquid 5 between the electrodes 2 and 4, provided with the insulating layers 6 and 7 in a conventional manner, and sealing the periphery of the transparent substrates 1 and 3 with an adhesive.

[0011] When a DC voltage is applied to the electrodes 2 and 4 of the above-described display device, the insulating layers 6 and 7 formed on the surfaces of the electrodes 2 and 4 act as an equivalent capacitance connected in series to an equivalent resistance of the display liquid 5 and thus suppress inflow of DC current components from the display liquid 5 to the electrodes 2 and 4 and outflow.

[0012] In other words, when the electrodes 2 and 4 are coated with the insulating layers 6 and 7 formed of a highly dielectric material, the film thickness is made sufficiently small to make the equivalent series capacitance sufficiently large, so as to make the time constant determined by the

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equivalent series capacitance and the specific resistance of the display liquid 5 sufficiently larger than the response time of the display device, the voltage applied to the display device within the response time can be maintained to be constant and substantially equal to the applied voltage. Moreover, since the display liquid 5 is not in contact with the electrodes 2 and 4, the above-described outflow of the charges can be restricted.

[0013] The equivalent capacitance can be set to be large. However, needless to say, when operating voltage pulses having the same polarity are continuously applied, the coating film is charged and thus the level of the voltage applied to the display device is reduced. As a result, the display function of the display device cannot be maintained.

[0014] An electrophoresis display device according to the present invention has a memory function which is perfect for display and thus does not require application of voltages of the same polarity for maintaining the display. Therefore, application of the pulses of the same polarity can be restricted as in the case of the technology described in Japanese Patent Application No. 2-8875. Accordingly, the present invention can be carried out in consideration of the response time of the display; and the dielectric constant, thickness and the withstand voltage of the coating films formed of the insulating layers 6 and 7. Thus, the electrochemical reaction of the display device can be restricted. Hereinafter, the case in which the above-described insulating layer is formed on one of the electrodes will be described.

[0015] This type or similar types of insulating layers or protection layers of an insulating substance formed on elec-

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trode surfaces are known in the case of AC-driven display devices. An insulating layer according to the present invention, which is required to have capabilities specific to DC-driven display devices, is essentially different from the conventional insulating layers. In other words, insulating layers in the AC-driven display devices include, for example, a polymer alignment layer used in liquid crystal display devices and a magnesium oxide protection layer used in AC plasma display devices. The former is used for alignment, and the latter has a function as an ion impact alleviator or a secondary electron supplying layer, which are different from formation of a capacitance for inhibiting a DC current. The withstand voltage V_{ix} required for a conventional protection layer is expressed by the following expression and is different from that of the insulating layer according to the present invention.

$$V_{ix} = \frac{\epsilon_0/d_0}{\epsilon_{ix}/d_{ix} + \epsilon_0/d_0} \times E$$

where E is the peak value of the applied AC voltage, ϵ_{ix} is the dielectric constant of the protection layer, d_{ix} is the thickness of the protection layer, ϵ_0 is the dielectric constant of the display liquid, and d_0 is the thickness of the display liquid.

[0016] The voltages applied to the insulating layer and the display liquid 5 for generating a display by applying the voltages to the display device according to the present invention can be found by obtaining a transient response resolution with the equivalent circuit shown in Figure 2. When a display pulse having a voltage E and a pulse width t_{pe} which is required for display is applied in the state of zero initial charge, voltage Vp applied to the display liquid 5 is expressed by:

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$$V_t = -\frac{C_1 E}{C_1 + C_2} \exp\left(-\frac{C_1 + C_2}{r C_1 C_2} t\right) + \frac{C_1 E}{C_1 + C_2} \exp\left(-\frac{1}{R(C_1 + C_2)} t\right)$$

where C_1 is an equivalent capacitance of the insulating layer, C_2 is an equivalent capacitance of the display device, R is an equivalent resistance of the display liquid 5, r is an intra-power supply resistance and wire equivalent resistance, and E and $-E$ are each an operating voltage pulse.

[0017] In the above expression, when r based on the wire and intra-power supply resistance is small, the first term of the expression is a transient term corresponding to the boosting period from a voltage of zero and thus becomes a negligible value within a short time period. Consequently, the value of the second term is applied to the display liquid 5. In the case where the C_1 , i.e., the capacitance of the insulating layer is designed to be large so that $R(C_1 + C_2)$ is sufficiently larger than the pulse width t_{pe} required for display, V_p is substantially $C_1 E / (C_1 + C_2)$ where $C_1 \gg C_2$. Accordingly, $V_p = E$. This value of V_p is equal to that in the case where no insulating layer is provided. Characteristically, unlike the case with no insulating layer, the pigment particles in the display liquid and electrons and ions in the solvent do not provide the electrode with charges, and only the spatial positions thereof are changed without the quality thereof being changed by oxidation or reduction reaction.

[0018] A voltage V_t applied on the insulating layer is determined in a similar manner, and the maximum value thereof is:

$$V_t = E - \frac{C_1 E}{C_1 + C_2} \exp\left(-\frac{1}{R(C_1 + C_2)} t_{tt}\right)$$

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The insulating layer structured according to the present invention needs to be designed to have a withstand voltage equal to or greater than V_i .

[0019] The expression can be represented as follows using the area S of the insulating layer, the thickness d_i of the insulating layer, the dielectric constant ϵ_i of the insulating layer, the area S of the display liquid, the thickness d_o of the display liquid, the dielectric constant ϵ_o of the display liquid, and the specific resistance R_o of the display liquid.

$$V_i = E - EX \frac{d_o \cdot \epsilon_i}{d_o \cdot \epsilon_i + d_i \cdot \epsilon_o} \exp \left(- \frac{t_{i1}}{(\epsilon_o + \epsilon_i \cdot d_o / d_i) R_o} \right)$$

Namely, the dielectric breakdown strength of the insulating layer $\times d_i > V_i$.

[0020] When, for example, an SiO_2 insulating layer having $\epsilon_i = 4 \times 0.0885 \times 10^{-12} (\text{F/cm})$ and $d_i = 0.1 \times 10^{-4} (\text{cm})$ is provided in an electrophoresis display device having $d_o = 50 \times 10^{-4} (\text{cm})$, $\epsilon_o = 3 \times 0.0885 \times 10^{-12} (\text{F/cm})$, $R_o = 10^{11} (\Omega \cdot \text{cm})$ and a response speed of $100 \times 10^{-3} (\text{sec.})$, V_i is about -0.28 V . By contrast, the dielectric breakdown strength of the insulating material is about 30 kV/mm , and the insulation strength of the insulating layer is 3 V , which are sufficient to withstand V_i . The constants regarding the electrophoresis display device calculated above by way of trial are average constants of trial products produced so far. Even materials having constants different from the above constants can be selected in consideration of the dielectric constant and dielectric breakdown strength of the insulating layer to be formed.

[0021] Hereinafter, voltages of various parts obtained when a pause pulse having a voltage of zero is applied after the

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above-described display pulse is applied will be described. Where t is the time period after the applied voltage is made zero, the voltage V_p applied to the display liquid is

$$V_p = -\frac{C_1 E}{C_1 + C_2} \left(1 - \exp\left(-\frac{1}{R(C_1 + C_2)} t_{r1}\right) \right) \exp\left(-\frac{1}{R(C_1 + C_2)} t\right)$$

Since C_1 is set so that $R(C_1 + C_2)$ is sufficiently larger than t_{r1} , the absolute value of V_p is too small to have any influence on the display. When R_s which is larger than R is set to restrict the charge inflow from C_1 to C_2 in order to alleviate the application of the opposite polarity voltage on the display device, as shown in Figure 3, the value of V_p can be further reduced.

[0022] The display device including the above-described insulating layer is driven effectively utilizing the memory for display so that the superimposing application of the voltage pulses having the same polarity, which is described in Japanese Patent Application No. 2-8875, is prevented and thus the excessive charging of the insulating layer is prevented. Accordingly, an element which has been once displayed is supplied with a voltage having an opposite polarity for discharging the charges of the insulating layer, and thus the voltage applied to the insulating layer does not exceed the value of V_i described above.

[0023] In the above, required conditions for the insulating layer and specific examples of the driving methods for a DC-driven planar display device including an insulating layer on either one of the electrodes 2 and 4 are described. The counter electrode can also be coated with a similar insulating layer, and this is preferable for improvement of performance, which is an objective of the present invention.

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[0024] An electrophoresis display device is described in the above as an example of DC-driven planar display devices, but the present invention is also applicable to other types of DC-driven planar display devices having a memory function such as, for example, an electrochromic display device.

[Effect of the Invention] A DC-driven planar display device according to the present invention, as can be apparent from the above description, coats a surface of an electrode in contact with the display liquid with an insulating material to block the DC current. Thus, quality change caused by transfer of charges between the display liquid and the electrode, i.e., the quality change of the display liquid caused by oxidation and reduction of ions in the display liquid can be prevented.

[0026] Accordingly, the color change of the dye, deterioration of the dispersion state, change of the electrophoresis characteristics of the dispersion particles, and the like can be prevented.

[Brief Description of the Drawings]

[Figure 1] A conceptual cross-sectional view of a DC-driven planar display device structured according to one example of the present invention.

[Figure 2] An equivalent circuit diagram in the case where an insulating layer is formed on one of the electrodes in Figure 1.

[Figure 3] An equivalent circuit diagram in the case where an external resistor is provided in Figure 2.

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[Figure 4] A conceptual enlarged cross-sectional view of an important part of a conventional electrophoresis display device.

[Description of Reference Numerals]

- 1 Transparent substrate
- 2 Electrode
- 3 Transparent substrate
- 4 Electrode
- 5 Display liquid
- 6 Insulating layer
- 7 Insulating layer

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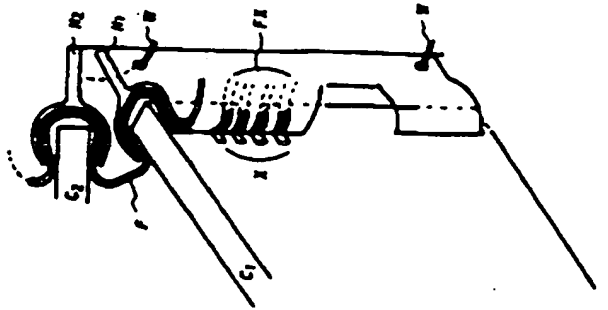
[Abstract]

[Objective] To provide a DC-driven planar display device, such an electrophoresis display device, for preventing a quality change of a display liquid caused by transfer of charges between the display liquid and an electrode, by blocking a DC current by coating a surface of the electrode in contact with the display liquid with an insulating material.

[Structure] A planar display device including a liquid or solid, the optical characteristics of which are changed by a DC current, provided in a sealed state between electrodes opposed to each other. The planar display device includes an insulating layer formed on a surface of at least one of the electrodes which are opposed to each other, and is structured so that the maximum value of the voltage applied to the insulating layer is larger than the product of the dielectric breakdown strength and the thickness of the insulating layer.

Patent Abstracts of Japan

PUBLICATION NUMBER : JP59098227
 PUBLICATION DATE : 06-06-84
 APPLICATION NUMBER : JP820208248
 APPLICATION DATE : 26-11-82

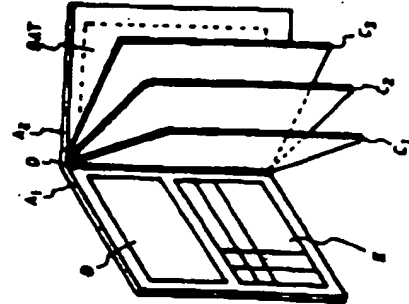


VOL: 8 NO: 214 (P - 304)
 AB. DATE : 29-09-1984 PAT: A 59098227
 PATENTEE : SHARP KK
 PATENT DATE: 06-06-1984

INVENTOR : NISHIMURA KOUSUKE

INT.CL. : G06F1/00; G06F15/02

TITLE : POCKETBOOK-TYPE ELECTRONIC
 COMPUTER



ABSTRACT : **PURPOSE:** To attain excellent portability and operability and increase the number of degrees of freedom for improvement of system, by connecting card-type computer constituting elements into a pocketbook type and making them attachable to and detachable from a pocketbook constituting body electrically and mechanically.

CONSTITUTION: A cover A1 and a back cover A2 of a pocketbook are formed by folding a sheet-shaped soft part, and a paper-shaped liquid crystal display device D and a touch-type thin keyboard K are arranged on the inside of the cover A1. A CPU is incorporated in a part of the inside of the cover A1 or the inside of the display device D or the keyboard K, and a paper-shaped battery BAT is incorporated in the back cover A2. Card-type computer constituting elements C1-C3 consist of, for example, an ROM, an RAM, etc., and their terminals X are brought elastically into contact with a bus line FX of a flexible print substrate F pressed in between a pair of pawls of holder parts H1 and H2. Thus, cards and the CPU are connected electrically and are attached and detached mechanically.

Patent Abstracts of Japan

PUBLICATION NUMBER : 60189731
PUBLICATION DATE : 27-09-85

APPLICATION DATE : 09-03-84
APPLICATION NUMBER : 59046366

APPLICANT : NIPPON PAINT CO LTD;

INVENTOR : NAKAJIMA KOJI;

INT.CL. : G02F 1/19 C09B 67/08 C09C 3/10 G09F 9/00

TITLE : DISPERSION COMPOSITION FOR ELECTROPHORESIS DISPLAY DEVICE

ABSTRACT : PURPOSE: To obtain a dispersion compsn. having a long life without uneven display by dispersing and dissolving the pigment particles coated with a self-curing resin having the characteristic to be electrified positive as electrophoretic particles as well as long-chain monocarboxylic acid and anionic surface active agent into an org. dispersant.

CONSTITUTION: A compsn. prepd. by dispersing and dissolving the electrophoretic particles A prepd. by coating TiO_2 or colorable org. or inorg. pigment particles with a self-curing resin having the group which possesses proton site when dissociated, that is, the characteristic to be electrified positive, for example, an alkylated melamine resin, alkylated urea resin, etc. which cure without a catalyst, more particularly alkylated methylomelamine resin and aliphatic monocarboxylic acid of 12-24C in order to improve the positive electrification characteristic of the particles A together with an anionic surface active agent for the stable dispersion of the particles A in an insulating inert org. dispersant, for example, arom. or alicyclic hydrocarbon is prepd. The dispersion for an electrophoresis display element which has good contrast, excellent dispersion stability of the particles A, decreases power consumption and has high liquid resistance is thus obtd.

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Patent Abstracts of Japan

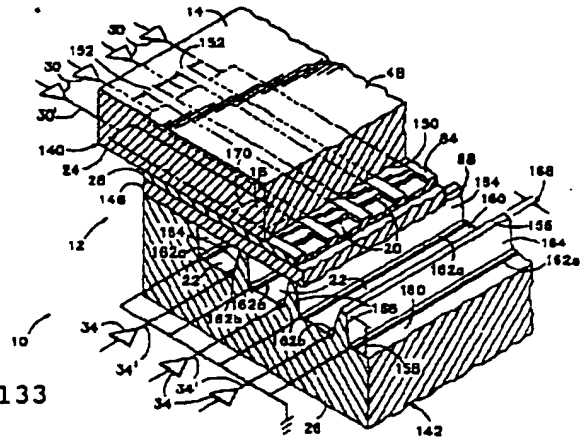
PUBLICATION NUMBER : JP6089081
 PUBLICATION DATE : 29-03-94
 APPLICATION NUMBER : JP930085609
 APPLICATION DATE : 19-03-93

VOL: 18 NO: 349 (P - 1763)
 AB. DATE : 30-06-1994 PAT: A 6089081
 PATENTEE : SONY TEKTRONIX CORP
 PATENT DATE: 29-03-1994

INVENTOR : TOOMASU ESU BUZATSUKU

INT.CL. : G09G3/36; G02F1/13; G02F1/133

TITLE : LIQUID CRYSTAL FLAT PANEL
 DISPLAY DEVICE



ABSTRACT : PURPOSE: To provide an electrooptic address assigning device with decreased crosstalk effect by incorporating a liquid crystal material showing such frequency reactivity that the dielectric anisotropy for the signal frequency higher than the critical frequency is almost zero.
 CONSTITUTION: The line electrode 20 is formed on the principal plane of a non-conductive light transmitting substrate 24, and a groove 22 is formed on the principal plane of the second nonconductive light transmitting substrate 26. A layer 28 consisting of a frequency-reactive electrooptical material such as two-frequency nematic liquid crystal is interposed between a first nonconductive light transmitting substrate 24 and a second nonconductive light transmitting substrate 26. In this constitution, it is preferable that the dielectric anisotropy $\Delta\epsilon$ of the frequency-reactive liquid crystal material in the layer 28 is rather small for the signal frequencies higher than the specific frequency f_{th} , and more preferably, almost zero. Thereby, the frequency-reactive liquid crystal material does not substantially react with signals having higher frequencies than the critical frequency. This device includes a data driver which sends reversed data signal in a first period and non-reversed data signal in the second period to each picture element.